Iterative Learning Control – From a Controllability Point of View



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- 1. Introduction
- 2. State space model in the iteration domain
- 3. Controllability
- 4. Target path controllability
- 5. Lead-in
- 6. Conclusions



Introduction

- Iterative learning control (ILC) is a method to improve the control performance of iterative processes.
- System description at time t and ILC iteration k:

$$\begin{array}{c} \mathbf{r}(t) \longrightarrow \\ \mathbf{u}_k(t) \longrightarrow \\ \end{array} \begin{array}{c} \mathcal{S} \end{array} \longrightarrow \mathbf{y}_k(t)$$



Introduction

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- System description at time t and ILC iteration k:

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■ ILC control signal update:

$$\mathbf{u}_{k+1}(t) = \mathcal{F}\left(\{\mathbf{u}_k(i)\}_{i=0}^{N-1}, \{\mathbf{e}_k(i)\}_{i=0}^{N-1}\right), \quad t = 0, \dots, N-1,$$

• Goal: find an update function \mathcal{F} such that

$$\|\mathbf{e}_k(t)\| \to 0, \quad k \to \infty, \quad t = 0, \dots, N-1$$





 Convergence proofs usually use a batch formulation of the system

$$\overline{\mathbf{y}}_k = \mathbf{S}_{\mathbf{u}}\overline{\mathbf{u}}_k + \mathbf{S}_{\mathbf{r}}\overline{\mathbf{r}}.$$

In literature, specific ILC algorithms are shown to give zero error if S_u has full row rank.



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- In literature, specific ILC algorithms are shown to give zero error if S_u has full row rank.
- What does it mean that S_u has full row rank?
- Controllability in the iteration domain will be used to answer the question.
- The result will be extended to *target path controllability* (TPC), and "lead-in" will be introduced.



■ Linear time-invariant state space model:

$$\begin{aligned} \mathbf{x}(t+1) &= \mathbf{A}\mathbf{x}(t) + \mathbf{B}_{\mathbf{u}}\mathbf{u}(t) + \mathbf{B}_{\mathbf{r}}\mathbf{r}(t),\\ \mathbf{y}(t) &= \mathbf{C}\mathbf{x}(t). \end{aligned}$$

Batch formulation: $\overline{\mathbf{x}} = \begin{pmatrix} \mathbf{x}(1)^{\mathsf{T}} & \dots & \mathbf{x}(N)^{\mathsf{T}} \end{pmatrix}^{\mathsf{T}}$

$$\overline{\mathbf{x}} = \mathbf{\Phi} \mathbf{x}(0) + \mathbf{S}_{\mathbf{x}\mathbf{u}}\overline{\mathbf{u}} + \mathbf{S}_{\mathbf{x}\mathbf{r}}\overline{\mathbf{r}},$$
$$\overline{\mathbf{y}} = \mathbf{C}\overline{\mathbf{x}}.$$

At ILC iteration k and k+1 it holds

$$\overline{\mathbf{x}}_k = \mathbf{\Phi} \mathbf{x}(0) + \mathbf{S}_{\mathbf{x}\mathbf{u}}\overline{\mathbf{u}}_k + \mathbf{S}_{\mathbf{x}\mathbf{r}}\overline{\mathbf{r}},$$

$$\overline{\mathbf{x}}_{k+1} = \mathbf{\Phi} \mathbf{x}(0) + \mathbf{S}_{\mathbf{x}\mathbf{u}}\overline{\mathbf{u}}_{k+1} + \mathbf{S}_{\mathbf{x}\mathbf{r}}\overline{\mathbf{r}},$$

hence

$$\overline{\mathbf{x}}_{k+1} = \overline{\mathbf{x}}_k + \mathbf{S}_{\mathbf{x}\mathbf{u}}(\overline{\mathbf{u}}_{k+1} - \overline{\mathbf{u}}_k) = \overline{\mathbf{x}}_k + \mathbf{S}_{\mathbf{x}\mathbf{u}}\Delta_{\overline{\mathbf{u}}_k}.$$





• Controllability matrix:
$$\mathscr{C} = (\mathbf{S}_{\mathbf{xu}} \cdots \mathbf{S}_{\mathbf{xu}})$$

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State Controllability

• Controllability matrix:
$$\mathscr{C} = (\mathbf{S}_{\mathbf{x}\mathbf{u}} \cdots \mathbf{S}_{\mathbf{x}\mathbf{u}})$$

Theorem (State Controllability)

The system in the iteration domain is controllable if and only if

$$\operatorname{rank} \mathscr{C} = \operatorname{rank} \mathbf{S}_{\mathbf{xu}} = Nn_x \quad \Leftrightarrow$$

rank $\mathbf{B}_{\mathbf{u}} = n_x$

Corollary (State Controllability)

A necessary condition for the system in the iteration domain to be controllable is that $n_u \ge n_x$.

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Corollary (State Controllability)

A necessary condition for the system in the iteration domain to be controllable is that $n_u \ge n_x$.

- Very strict requirement for controllability.
- For ILC it is more relevant to see if the output is controllable.





Output Controllability

• Output controllability matrix: $\mathscr{C}^{\circ} = \mathcal{C}\mathscr{C} = (\mathcal{C}S_{xu} \cdots \mathcal{C}S_{xu})$





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A necessary condition for the system in the iteration domain to be controllable is that rank $\mathbf{B}_{\mathbf{u}} \ge n_y$.





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Corollary (Output Controllability)

A necessary condition for the system in the iteration domain to be controllable is that rank $\mathbf{B}_{\mathbf{u}} \ge n_y$.

■ Note that S_u = CS_{xu}, hence ||e_k(t)|| → 0 is possible if the system is output controllable.





Interpretation of controllability requirements

- A SISO system with state dimension n_x can require n_x time steps to reach the desired state.
 - $\Rightarrow~$ the first part of \overline{x} cannot be defined arbitrary.



Interpretation of controllability requirements

A SISO system with state dimension n_x can require n_x time steps to reach the desired state.

 \Rightarrow the first part of \overline{x} cannot be defined arbitrary.

\overline{x} must follow the dynamics in the time domain.

• Let
$$\mathbf{x} = \begin{pmatrix} p & v \end{pmatrix}^{\mathsf{T}}$$
 and

$$\mathbf{x}(t+1) = \begin{pmatrix} 1 & T_s \\ 0 & 1 \end{pmatrix} \mathbf{x}(t) + \begin{pmatrix} T_s^2/2 \\ T_s \end{pmatrix} u(t)$$

• If
$$p(t) = a$$
 and $v(t) = b$ then

$$p(t+1) = a + bT_s + T_s^2/2u(t)$$

$$v(t+1) = b + T_su(t)$$

• Hence not possible to choose $\mathbf{x}(t+1)$ arbitrary with only $u(t) \Rightarrow \overline{\mathbf{x}}$ cannot be chosen arbitrary.



Output controllability is achieved if p or v is chosen as output.
If instead

$$\mathbf{x}(t) = \begin{pmatrix} p(t) \\ v(t) \end{pmatrix}$$
$$\mathbf{x}(t+1) = \begin{pmatrix} 1 & T_s \\ 0 & 1 \end{pmatrix} \mathbf{x}(t) + \begin{pmatrix} 0 \\ T_s \end{pmatrix} u(t)$$

then the requirement for output controllability is not satisfied if p is chosen as output.

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Target Path Controllability (TPC)

- Extension to controllability to check if a system can follow a trajectory.
- The lead p states how many samples that might be required to go from the start point to the trajectory and the lag q states during how many samples the system can follow the trajectory.



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Theorem (Target Path Controllability)

An LTI system is TPC with lead p and lag q iff $\mathrm{rank}\,\mathbf{S_{yu}}(p,q)=qn_y,$ where

$$\mathbf{S}_{\mathbf{y}\mathbf{u}}(p,q) = \begin{pmatrix} \mathbf{C}\mathbf{A}^{p-1}\mathbf{B}_{\mathbf{u}} & \cdots & \mathbf{C}\mathbf{B}_{\mathbf{u}} & \cdots & 0\\ \vdots & \ddots & & \ddots & \vdots\\ \mathbf{C}\mathbf{A}^{p+q-2}\mathbf{B}_{\mathbf{u}} & & \cdots & & \mathbf{C}\mathbf{B}_{\mathbf{u}} \end{pmatrix}.$$





Remark

If $p = n_x$ and q = 1, then $\mathbf{S}_{\mathbf{yu}}(n_x, 1) = \mathscr{C}^o$, i.e., requirement for standard output controllability is obtained.

Theorem (Output Controllability vs. TPC)

Output controllability of the system in the iteration domain is equivalent to the system in the time domain being TPC with p=1 and q=N.



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If $p = n_x$ and q = 1, then $\mathbf{S}_{\mathbf{yu}}(n_x, 1) = \mathscr{C}^o$, i.e., requirement for standard output controllability is obtained.

Theorem (Output Controllability vs. TPC)

Output controllability of the system in the iteration domain is equivalent to the system in the time domain being TPC with p=1 and q=N.

Given the model

$$\mathbf{x}(t+1) = \begin{pmatrix} 1 & T_s \\ 0 & 1 \end{pmatrix} \mathbf{x}(t) + \begin{pmatrix} 0 \\ T_s \end{pmatrix} u(t)$$

then $S_{yu}(2, N-1)$ satisfies the requirement for TPC.





Lead-in

■ Use TPC to find *p*.

• Move the reference path $\tau \ge p$ samples forward in time.







Lead-in

• Use TPC to find p.

- Move the reference path $\tau \ge p$ samples forward in time.
- Append a new initial reference trajectory $\widetilde{\mathbf{r}}(t)$.
- The output now follows the new reference and
 - $\mathbf{e}(t)$ for $t \leq \tau$ is not of importance.
 - $\mathbf{e}(t) \rightarrow 0$ for $t > \tau$, given an ILC algorithm that makes the error converge to 0.





Conclusions

- Assumptions of the system used for convergence proofs are often too strict to be useful in practice.
- A commonly used requirement is equivalent to output controllability for a state space model in the iteration domain.
- Target path controllability of the system in the time domain is more relevant to investigate than controllability of the system in the iteration domain.



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- A commonly used requirement is equivalent to output controllability for a state space model in the iteration domain.
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